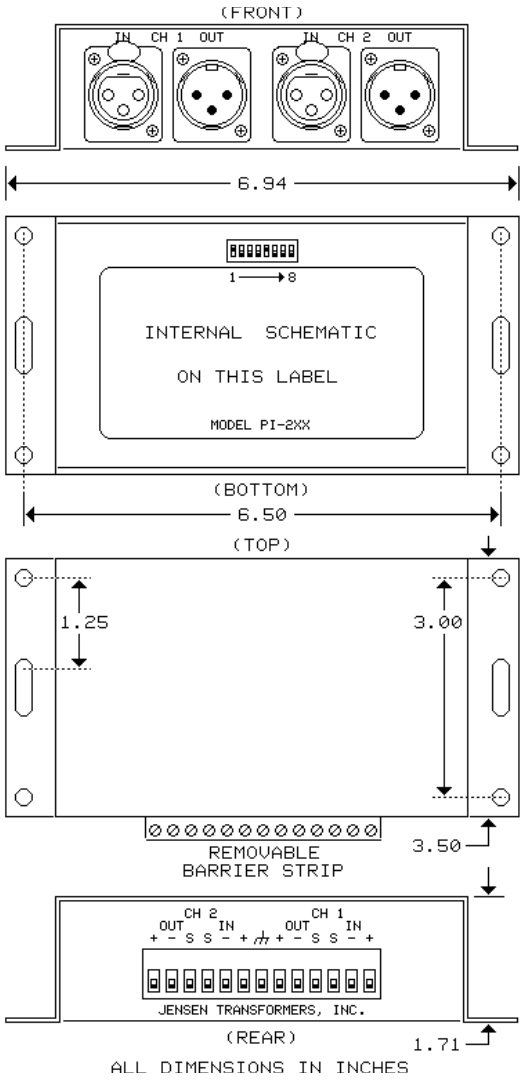
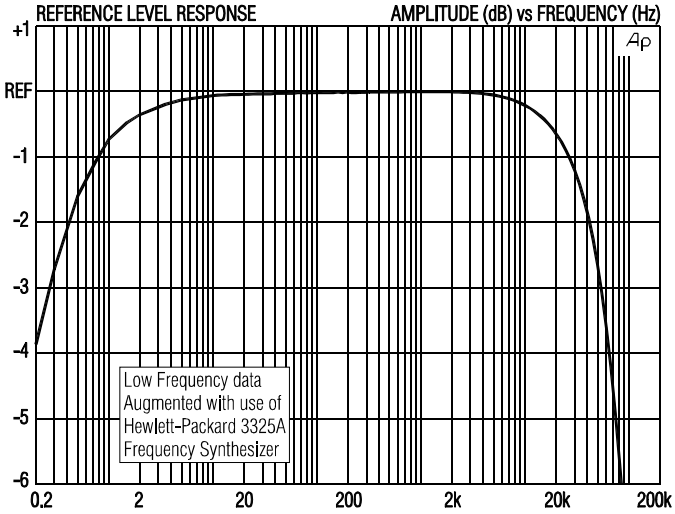
2-CHANNEL AUDIO GROUND ISOLATOR

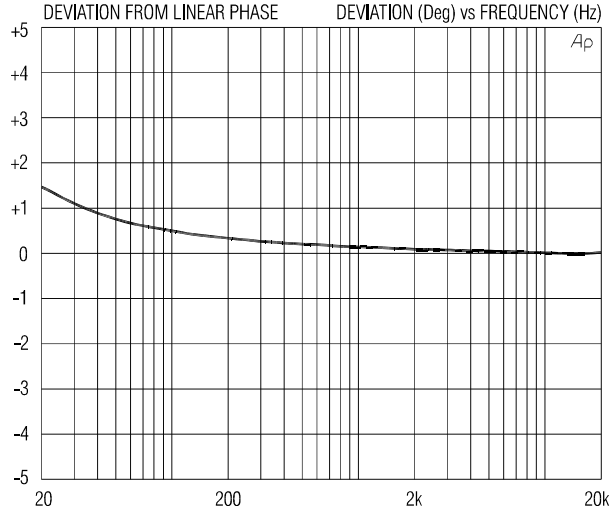
SOLVES MOST "TRANSFORMER-LESS" INPUT PROBLEMS

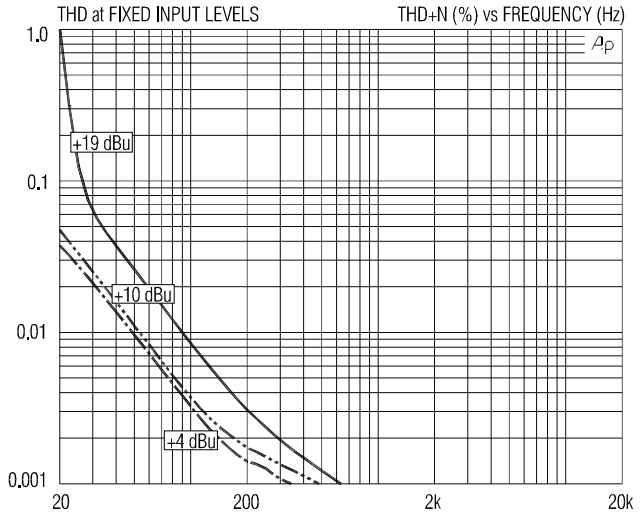
- **High CMRR when driven by *any source*, balanced *or* unbalanced**
- **Integral ground option switches solve other equipment problems**
- **Wide bandwidth: –3 dB at 0.25 Hz and 50 kHz**
- **Handles signals up to +19 dBu at 20 Hz or +23 dBu at 30 Hz**
- **Input impedances are 24 kΩ when driving typical 20 kΩ inputs**

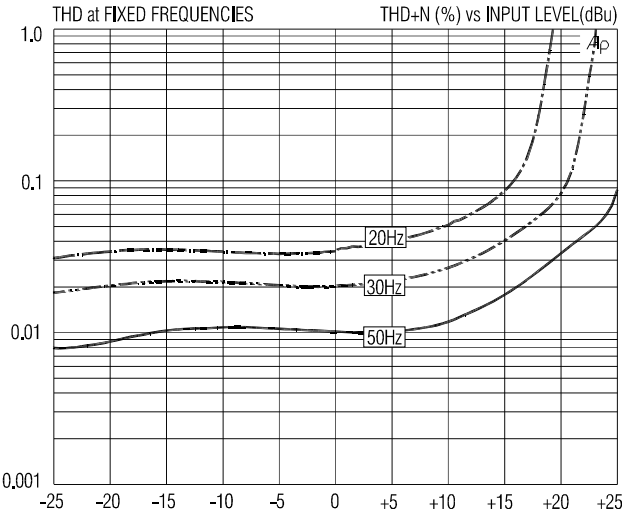
This isolator solves the poor CMRR (ground noise rejection), RFI susceptibility, and shield noise coupling problems common to "transformer-less" balanced inputs while maintaining audiophile signal quality. To avoid excessive high frequency losses, no more than 3 feet of cable should be used on each output.





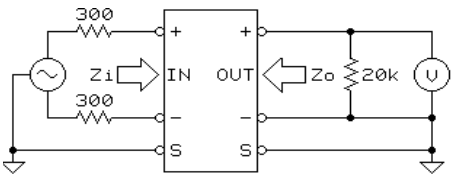




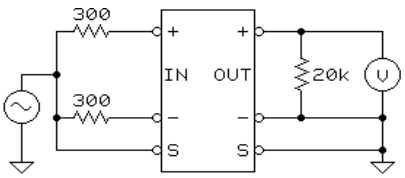


PI-2XX SPECIFICATIONS (all levels are input unless noted, +4 dBu = 1.23 V RMS)

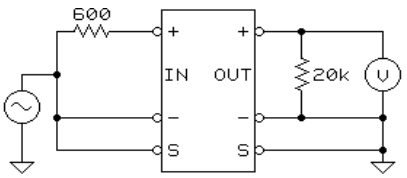
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Zi	1 kHz, +4 dBu, test circuit 1	22.0 kΩ	23.5 kΩ	25.0 kΩ
Insertion loss	1 kHz, +4 dBu, test circuit 1		1.6 dB	2.0 dB
Magnitude response, ref 1 kHz	20 Hz, +4 dBu, test circuit 1	–0.15 dB	–0.03 dB	±0.0 dB
	20 kHz, +4 dBu, test circuit 1	–1.0 dB	–0.70 dB	±0.0 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, +4 dBu, test circuit 1		+1.4/–0°	±2.0°
Distortion (THD)	1 kHz, +4 dBu, test circuit 1		<0.001%	
	20 Hz, +4 dBu, test circuit 1		0.04%	0.10%
Maximum 20 Hz input level	1% THD, test circuit 1	+17 dBu	+19 dBu	
Common-mode rejection ratio (CMRR) 600 Ω balanced / unbalanced source	60 Hz, test circuit 2 / 3		124 / 95 dB	
	3 kHz, test circuit 2 / 3	85 dB /	95 / 85 dB	
Output impedance, Zo	1 kHz, test circuit 1		4.65 kΩ	
DC resistances	input		2.26 kΩ	
	output		1.90 kΩ	
Capacitances	1 kHz, input to output shield and case		105 pF	
	1 kHz, output to output shield and case		115 pF	
Allowable source impedance	(output impedance of device driving the ISO-MAX input)	0	600 Ω	2 kΩ
Allowable load impedance	(input impedance of device loading the ISO-MAX output)	10 kΩ	20 kΩ	∞
Allowable load capacitance	(cable & input capacitance loading the ISO-MAX output)	0	50 pF	100 pF
Temperature range	operation or storage	0° C		70° C
Input to Output Voltage Difference (see IMPORTANT NOTE below)	any input to any output shield or any shield to case, 60 Hz			24 V RMS 34 V peak



TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.

IMPORTANT NOTE: THIS PRODUCT IS NOT INTENDED FOR USE IN CIRCUMSTANCES WHERE THE DC OR PEAK AC VOLTAGE BETWEEN INPUT AND OUTPUT CONNECTIONS EXCEEDS 34 VOLTS OR WHERE ITS FAILURE COULD CAUSE INJURY OR DEATH.

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