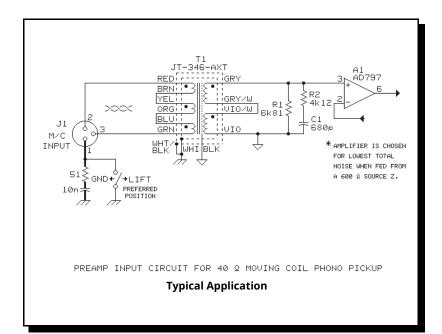
## **JT-346-AXT**

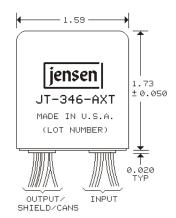
# **Moving Coil Transformer** 1:4 STEP-UP FOR MC CARTRIDGES UP TO 40 Ω

ensen

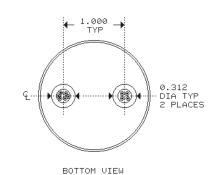
- Our highest performance moving coil input transformer
- Wide bandwidth: -3 dB at 0.3 Hz and 220 kHz
- Excellent time domain performance: DLP +1.2° typical
- Transformer Noise Figure only 1.5 dB @ 40 Ω, 2.2 dB @ 25 Ω
- High common-mode rejection: 135 dB at 60 Hz

This transformer, with about 700  $\Omega$  secondary source impedance, will give excellent noise performance with ultra-low e<sub>n</sub> amplifiers. The primary is fully balanced and its leads may be reversed to invert polarity. 60 dB nested double magnetic shielding is standard. See companion data sheet for 3  $\Omega$  or 5  $\Omega$  use.

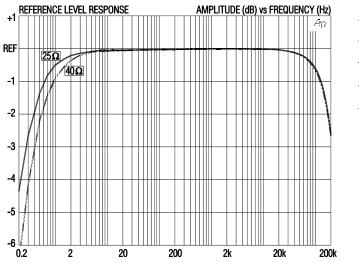


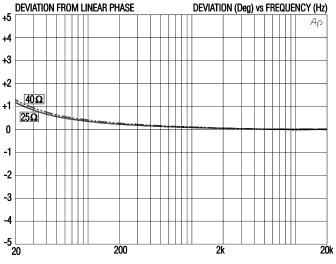


8 INCH #26 AWG (7x34) SILVER PLATED COPPER LEADS WITH TEFLON INSULATION



RECOMMENDED MOUNTING IS WITH VR-4 CLAMP (SUPPLIED WITH TRANSFORMER)





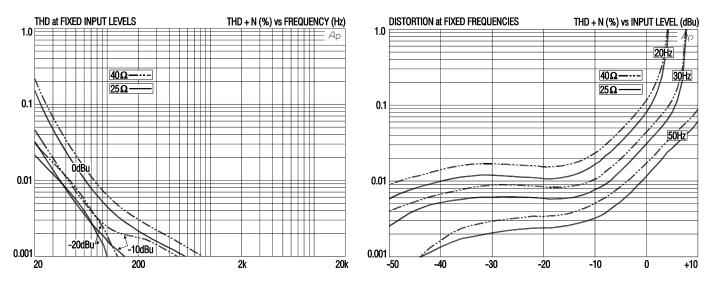


Jensen Transformers Inc., 9304 Deering Ave. Chatsworth, CA 91311 P: (818) 374-5857 F: (818) 374-5856 info@jensen-transformers.com

jensen-transformers.com



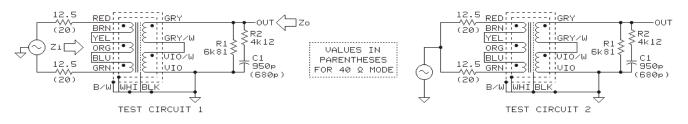
## JT-346-AXT



#### JT-346-AXT SPECIFICATIONS (unless noted, apply for either 25 Ω or 40 Ω sources; all levels are input)

PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Zi	1 kHz, -30 dBu, test circuit 1	395 Ω	417 Ω	435 Ω
Voltage gain	1 kHz, -30 dBu, test circuit 1	11.65 dB	11.77 dB	11.85 dB
Magnitude response, ref 1 kHz	20 Hz, -30 dBu, test circuit 1	-0.1 dB	-0.05 dB	±0.0 dB
	20 kHz, -30 dBu, test circuit 1	-0.1 dB	-0.03 dB	+0.1 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, -30 dBu, test circuit 1		+1.2°	±2°
Distortion (THD)	1 kHz, -30 dBu, test circuit 1		<0.001%	
	20 Hz, -30 dBu, test circuit 1		0.015%	0.05%
Maximum 20 Hz input level	1% THD, test circuit 1	+2.0 dBu	+4.1 dBu	
Common-mode rejection ratio (CMRR)	60 Hz, test circuit 2		135 dB	
	3 kHz, test circuit 2	95 dB	110 dB	
Output impedance, Zo	1 kHz, Rs = 25 $\Omega$ , test circuit 1		605 Ω	
	1 kHz, Rs = 40 Ω, test circuit 1		805 Ω	
DC resistances	3 primaries in series (per test circuits)		5.73 Ω	
	2 secondaries in series (GRY to VIO)		154 Ω	
Capacitances @ 1 kHz	3 primaries in series to shield and case		573 pF	
	2 secondaries to shield and case		417 pF	
Turns ratio	3 primaries in series (per test circuits)	1:4.070	1:4.075	1:4.080
Temperature range	operation or storage	0° C		70° C
Breakdown voltage (see IMPORTANT NOTE below)	primary or secondary to shield and case, 60 Hz, 1 minute test duration	250 V RMS		

IMPORTANT NOTE: This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.



All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.



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