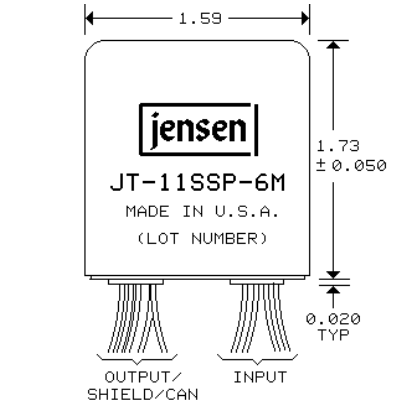
Line Input Transformer

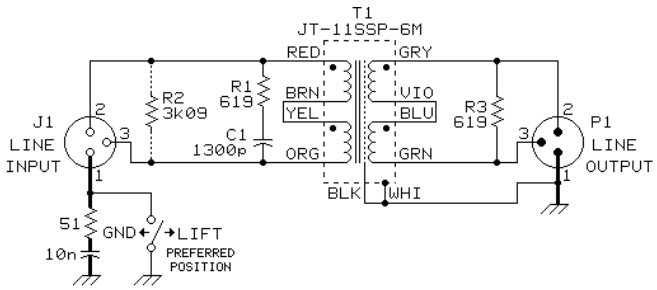
1:1 SPLIT WINDING 'REPEAT COIL'

- **Ideal for use with low input impedance circuits**
- **Wide bandwidth: -3 dB at 0.4 Hz and 220 kHz**
- **Recommended for levels up to +19 dBu at 20 Hz**
- **Insertion loss only 1.9 dB in 600 Ω to 600 Ω application**
- **High common-mode rejection: 110 dB at 60 Hz**

This transformer has split primary and secondary windings which can be connected for either 1:2 (150:600 Ω), 1:1 (600:600 Ω), or 2:1 (600:150 Ω) operation. Distortion, although specified for 600 Ω sources, will be further reduced by lower impedances. A 30 dB magnetic shield package is standard.



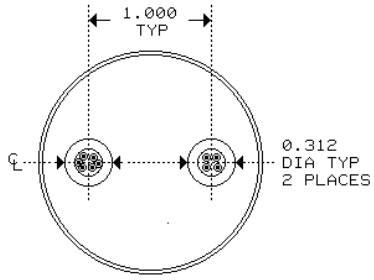
#30 AWG (7x38) UL STYLE 1061 COLOR CODED WIRE LEADS, 8" MINIMUM LENGTH



R1 and C1 flatten input impedance vs frequency
Optional R2 lowers input impedance to exactly 600 Ω
R3 is used for ≥10 kΩ load and omitted for 600 Ω load

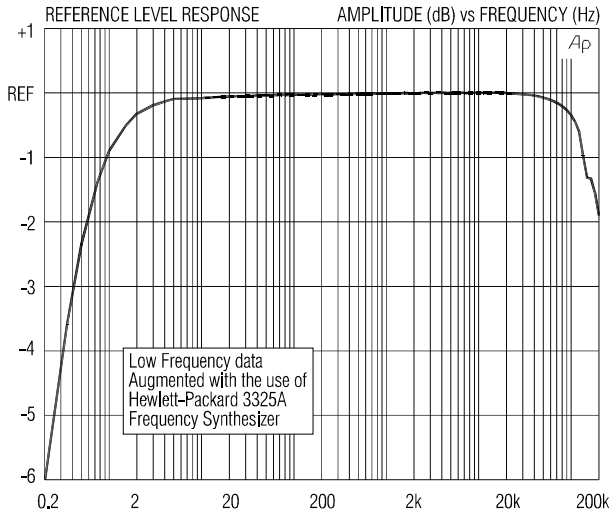
GROUND ISOLATOR TO TERMINATE A 600 Ω BALANCED LINE

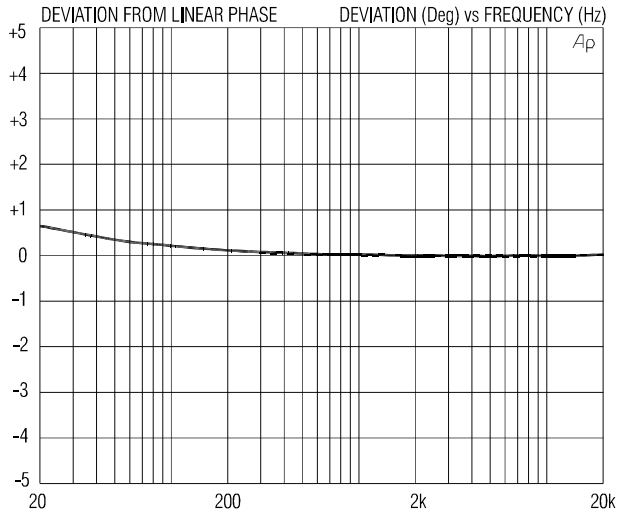
Typical Application

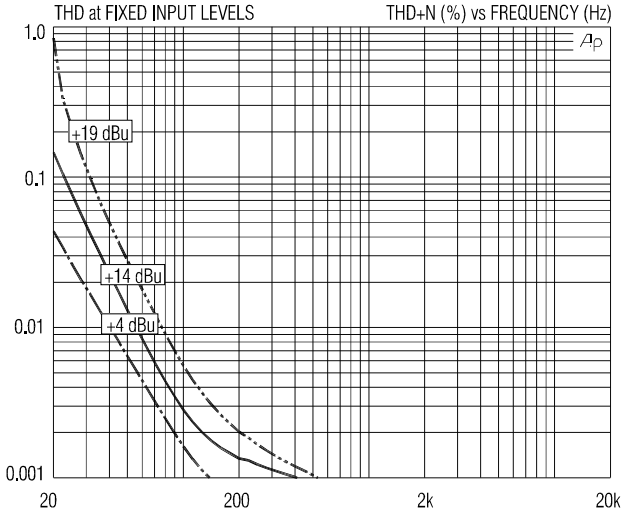


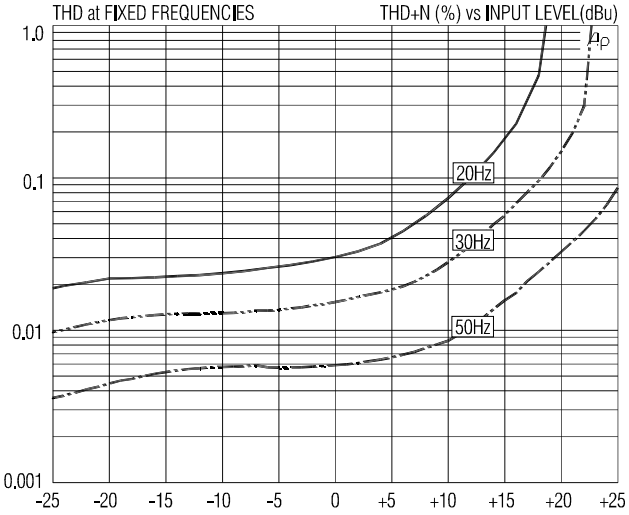
BOTTOM VIEW

RECOMMENDED MOUNTING IS WITH VR-4 CLAMP
(SUPPLIED WITH TRANSFORMER)





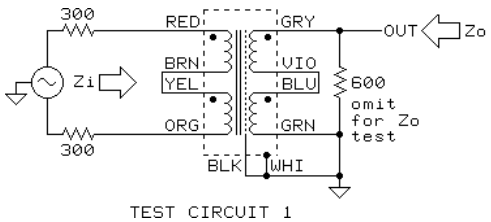




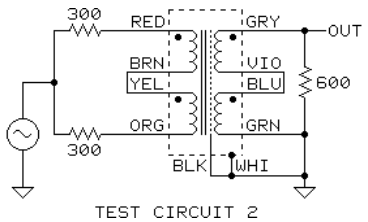
JT-11SSP-6M SPECIFICATIONS (1:1 series/series configuration, all levels are input unless noted)

PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Zi	1 kHz, +4 dBu, test circuit 1	738 Ω	745 Ω	752 Ω
Voltage gain	1 kHz, +4 dBu, test circuit 1	–2.1 dB	–1.9 dB	–1.7 dB
Magnitude response, ref 1 kHz	20 Hz, +4 dBu, test circuit 1, Rs=600 Ω	–0.2 dB	–0.05 dB	±0.0 dB
	20 kHz, +4 dBu, test circuit 1, Rs=600 Ω	–0.1 dB	–0.01 dB	+0.1 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, +4 dBu, test circuit 1, Rs=600 Ω		+0.6/–0°	±2.0°
Distortion (THD)	1 kHz, +4 dBu, test circuit 1, Rs=600 Ω		<0.001%	
	20 Hz, +4 dBu, test circuit 1, Rs=600 Ω		0.04%	0.10%
Maximum 20 Hz input level	1% THD, test circuit 1, Rs=600 Ω	+17 dBu	+19 dBu	
Input Common-mode rejection (CMRR) 600 Ω balanced source	60 Hz, test circuit 2		110 dB	
	3 kHz, test circuit 2	60 dB	75 dB	
Output Common-mode rejection (CMRR) 600 Ω balanced load	60 Hz, test circuit 3		79 dB	
	3 kHz, test circuit 3		45 dB	
Output impedance, Zo	1 kHz, test circuit 1, Rs=600 Ω		745 Ω	
DC resistances	total primary (RED to ORG)		32 Ω	
	total secondary (GRY to GRN)		113 Ω	
Capacitances @ 1 kHz	total primary to shield and case		950 pF	
	total secondary to shield and case		1.22 nF	
Turns ratio	any winding to any other winding	0.999:1	1.000:1	1.001:1
Temperature range	operation or storage	0° C		70° C
Breakdown voltage (see IMPORTANT NOTE below)	primary or secondary to shield and case, 60 Hz, 1 minute test duration	250 V RMS		

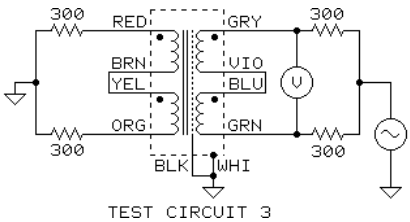
IMPORTANT NOTE: This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.



TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.